

| Ref # | Hits | Search Query   | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|--|---|------------------|---------|------------------|
| L1    | 202  | verif\$6 same (intellectual adj property or IP) same design    | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2005/11/02 13:45 |
| L2    | 20   | L1 and simulat\$3 same time same module                        | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2005/11/02 13:41 |
| L3    | 179  | verif\$6 same (intellectual adj property or IP) same design\$3 | US-PGPUB                                    | OR               | ON      | 2005/11/02 14:26 |
| L4    | 45   | L3 and @ad<"20011114"  | US-PGPUB                                    | OR               | ON      | 2005/11/02 13:51 |
| L5    | 16   | L4 and time same simulat\$6                                    | US-PGPUB                                    | OR               | ON      | 2005/11/02 13:51 |
| L6    | 34   | 703/? and intellectual adj property                            | US-PGPUB                                    | OR               | ON      | 2005/11/02 14:32 |
| L7    | 19   | L6 and @ad<"20011114"  | US-PGPUB                                    | OR               | ON      | 2005/11/02 14:12 |
| L8    | 3    | 703/19 and intellectual adj property                           | US-PGPUB                                    | OR               | ON      | 2005/11/02 14:12 |
| L9    | 14   | 703/13 and intellectual adj property                           | US-PGPUB                                    | OR               | ON      | 2005/11/02 14:12 |
| L10   | 5    | L9 and @ad<"20011114"  | US-PGPUB                                    | OR               | ON      | 2005/11/02 14:13 |
| L11   | 129  | 716/? and intellectual adj property                            | US-PGPUB                                    | OR               | ON      | 2005/11/02 14:12 |
| L12   | 42   | L11 and @ad<"20011114"   | US-PGPUB                                    | OR               | ON      | 2005/11/02 14:32 |
| L13   | 12   | L12 and vector and simulat\$3 same tim\$3                      | US-PGPUB                                    | OR               | ON      | 2005/11/02 14:15 |
| L16   | 2    | "6380764".pn.  | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2005/11/02 14:29 |
| L17   | 2    | "6769110".pn.  | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2005/11/02 14:28 |
| L18   | 2    | "6320421".pn.  | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2005/11/02 14:30 |
| L19   | 59   | 713/? and intellectual adj property                            | US-PGPUB                                    | OR               | ON      | 2005/11/02 14:32 |
| L20   | 29   | L19 and @ad<"20011114"   | US-PGPUB                                    | OR               | ON      | 2005/11/02 14:32 |

Day : Wednesday

Date: 11/2/2005

Time: 12:49:23

**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = AKITA

First Name = YOHEI

| Application#                    | Patent#    | Status | Date Filed | Title  | Inventor Name |
|---------------------------------|------------|--------|------------|--|---------------|
| <a href="#"><u>09582327</u></a> | 6380764    | 150    | 06/23/2000 | SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, RECORDING MEDIUM STORED WITH CELL LIBRARY, AND METHOD FOR DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT      | AKITA, YOHEI  |
| <a href="#"><u>09583720</u></a> | 6320421    | 150    | 05/30/2000 | Logic circuit  | AKITA, YOHEI  |
| <a href="#"><u>09946440</u></a> | Not Issued | 95     | 09/06/2001 | LOGIC CIRCUIT  | AKITA, YOHEI  |
| <a href="#"><u>09987317</u></a> | Not Issued | 71     | 11/14/2001 | System verification equipment, system verification method and LSI manufacturing method using the system verification equipment                     | AKITA, YOHEI  |
| <a href="#"><u>10084435</u></a> | 6769110    | 150    | 02/28/2002 | SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, STORAGE MEDIUM ON WHICH CELL LIBRARY IS STORED AND DESIGNING METHOD FOR SEMICONDUCTOR INTEGRATED CIRCUIT  | AKITA, YOHEI  |
| <a href="#"><u>10790797</u></a> | Not Issued | 30     | 03/03/2004 | Logic circuit and program for executing thereon  | AKITA, YOHEI  |
| <a href="#"><u>10827291</u></a> | Not Issued | 30     | 04/20/2004 | Semiconductor integrated circuit device, storage medium on which cell library is stored and designing method for semiconductor intergrated circuit | AKITA, YOHEI  |
| <a href="#"><u>10886616</u></a> | Not Issued | 30     | 07/09/2004 | Semiconductor integrated circuit   | AKITA, YOHEI  |

## Inventor Search Completed: No Records to Display.

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|                                 |                                       |                                    |
|---------------------------------|---------------------------------------|------------------------------------|
| <b>Search Another: Inventor</b> | <b>Last Name</b>                      | <b>First Name</b>                  |
|                                 | <input type="text" value="akita"/>    | <input type="text" value="yohei"/> |
|                                 | <input type="button" value="Search"/> |                                    |

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